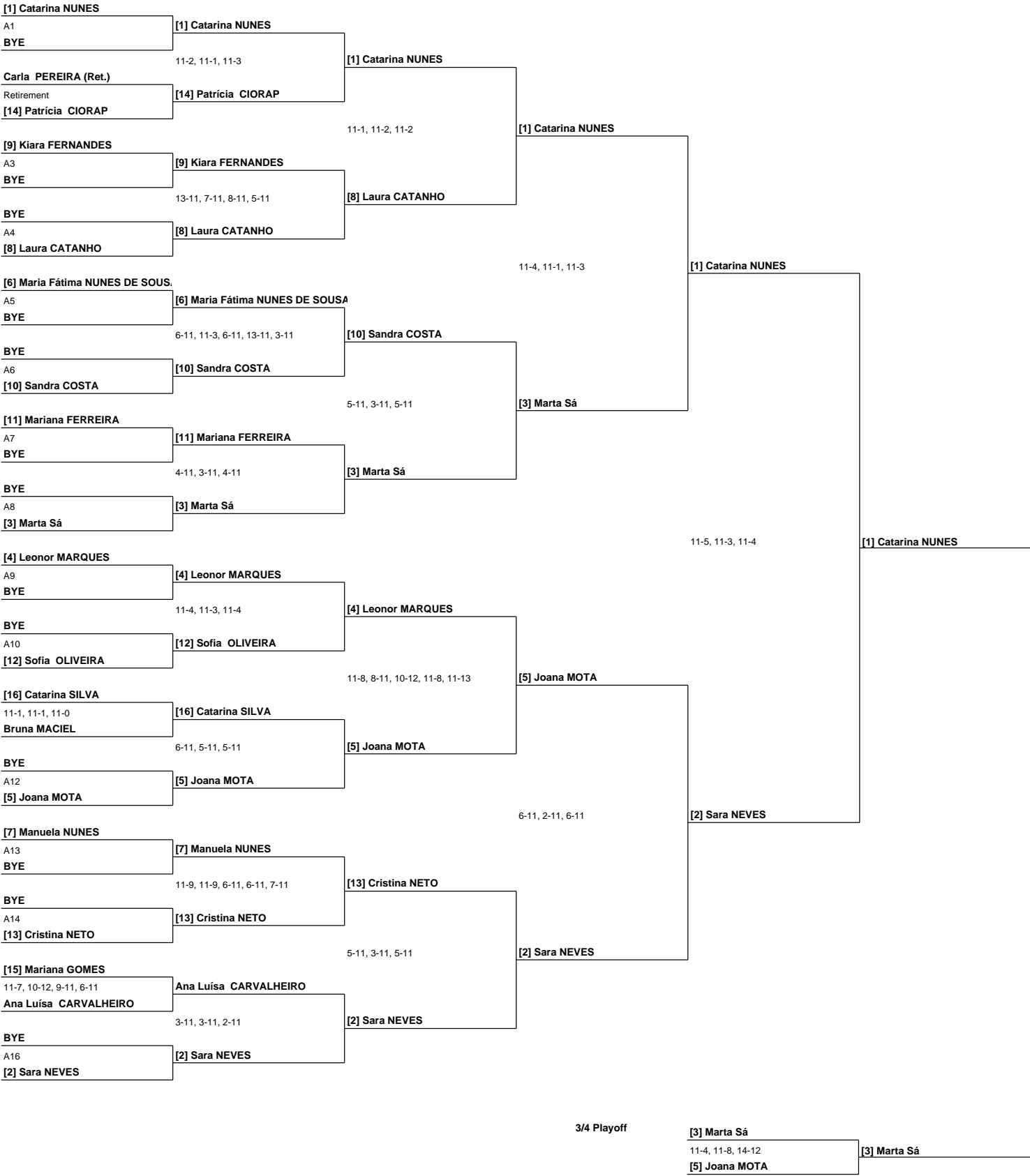
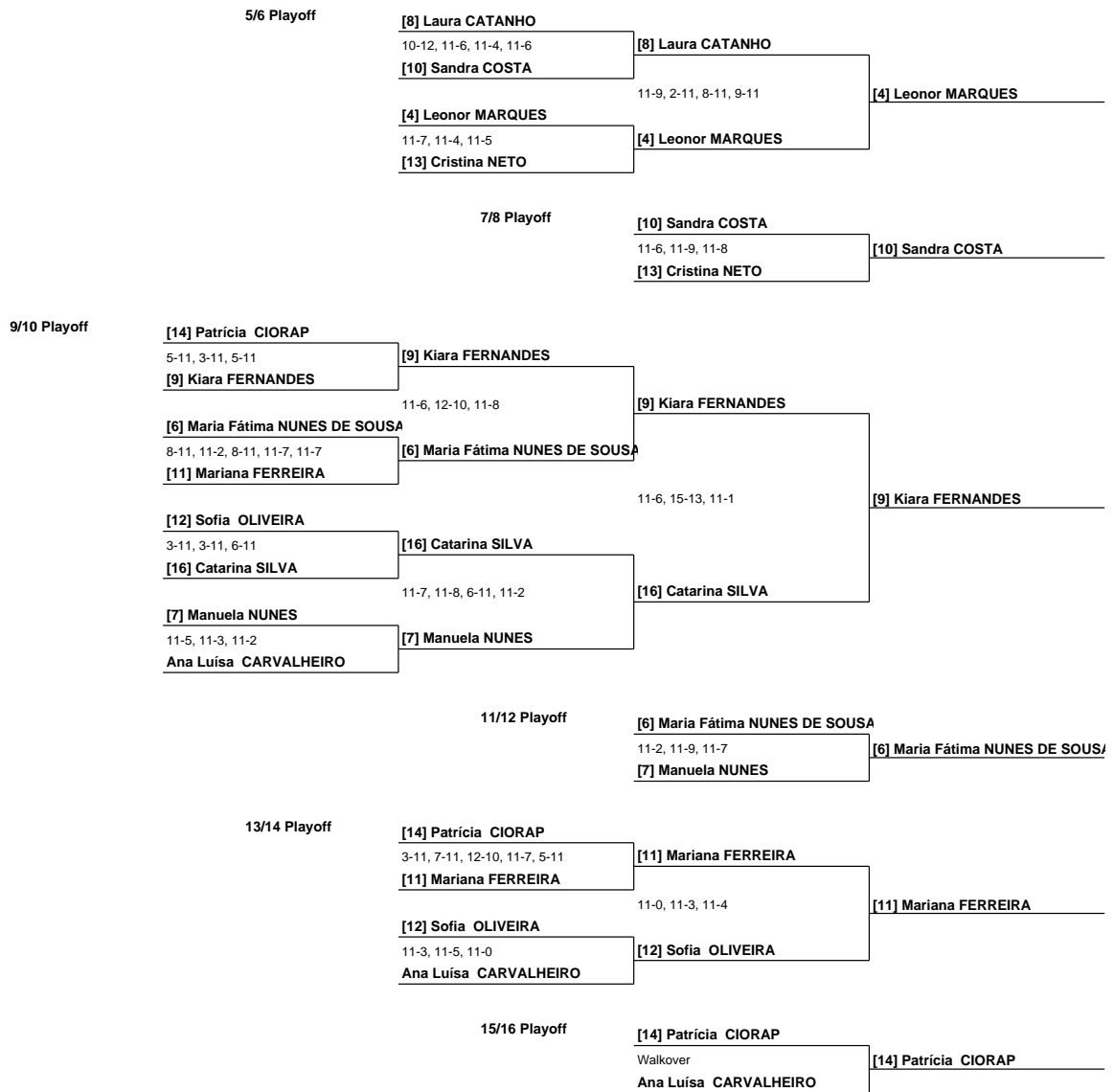


2022/2023 - Coimbra Squash Open (Nível 4)

F1

Main Draw





2019-2020

Round 1

Player	Opponent	Score
I1	BYE	
I2	BYE	
I3	BYE	
I4	BYE	
I5	BYE	
I6	Bruna MACIEL	
I7	BYE	
I8	[15] Mariana GOMES	
I9	BYE	
I10	BYE	
I11	Bruna MACIEL	
I12	[15] Mariana GOMES	
I13	BYE	
I14	BYE	
I15	BYE	
I16	[15] Mariana GOMES	

Round 2

Player	Opponent	Score
I1	I9	
I2	I10	
I3	I11	
I4	I12	
I5	I13	
I6	I14	
I7	I15	
I8	I16	

Final

Player	Opponent	Score
I1	I8	

BYE	
J1	Bruna MACIEL
Bruna MACIEL	

The diagram shows a 4-bit bus system. On the left, there are two 2-bit registers, K1 and K2, each labeled 'BYE'. On the right, there is a 4-bit output bus labeled 'BYE'. The registers K1 and K2 are connected to the output bus via a 4-bit bus labeled 'K3'.

BYE	
L1	
BYE	

```

graph RL
    Root[ ] --- M7[M7]
    Root --- M6[M6]
    M7 --- M5a[M5]
    M7 --- M5b[M5]
    M6 --- M3c[M3]
    M6 --- M3d[M3]
    M5a --- M3e[M3]
    M5a --- M3f[M3]
    M5b --- M3g[M3]
    M5b --- M3h[M3]
    M3c --- M2i[M2]
    M3c --- M2j[M2]
    M3d --- M2k[M2]
    M3d --- M2l[M2]
    M3e --- M2m[M2]
    M3e --- M2n[M2]
    M3f --- M2o[M2]
    M3f --- M2p[M2]
    M3g --- M2q[M2]
    M3g --- M2r[M2]
    M3h --- M2s[M2]
    M3h --- M2t[M2]
    M2i --- M1u[M1]
    M2i --- M1v[M1]
    M2j --- M1w[M1]
    M2j --- M1x[M1]
    M2k --- M1y[M1]
    M2k --- M1z[M1]
    M2l --- M1aa[M1]
    M2l --- M1ab[M1]
    M2m --- M1ac[M1]
    M2m --- M1ad[M1]
    M2n --- M1ae[M1]
    M2n --- M1af[M1]
    M2o --- M1ag[M1]
    M2o --- M1ah[M1]
    M2p --- M1ai[M1]
    M2p --- M1aj[M1]
    M2q --- M1ak[M1]
    M2q --- M1al[M1]
    M2r --- M1am[M1]
    M2r --- M1an[M1]
    M2s --- M1ao[M1]
    M2s --- M1ap[M1]
    M2t --- M1aq[M1]
    M2t --- M1ar[M1]
    M3c --- M0sa[M0]
    M3c --- M0sb[M0]
    M3d --- M0sc[M0]
    M3d --- M0sd[M0]
    M3e --- M0se[M0]
    M3e --- M0sf[M0]
    M3f --- M0sg[M0]
    M3f --- M0sh[M0]
    M3g --- M0si[M0]
    M3g --- M0sj[M0]
    M3h --- M0sk[M0]
    M3h --- M0sl[M0]
    M2i --- M0sm[M0]
    M2i --- M0sn[M0]
    M2j --- M0so[M0]
    M2j --- M0sp[M0]
    M2k --- M0sq[M0]
    M2k --- M0sr[M0]
    M2l --- M0ss[M0]
    M2l --- M0st[M0]
    M2m --- M0su[M0]
    M2m --- M0sv[M0]
    M2n --- M0sw[M0]
    M2n --- M0sx[M0]
    M2o --- M0sy[M0]
    M2o --- M0sz[M0]
    M2p --- M0ta[M0]
    M2p --- M0tb[M0]
    M2q --- M0tc[M0]
    M2q --- M0td[M0]
    M2r --- M0te[M0]
    M2r --- M0tf[M0]
    M2s --- M0tg[M0]
    M2s --- M0th[M0]
    M2t --- M0ti[M0]
    M2t --- M0tj[M0]
    M2u --- M0tk[M0]
    M2u --- M0tl[M0]
    M2v --- M0tm[M0]
    M2v --- M0tn[M0]
    M2w --- M0to[M0]
    M2w --- M0tp[M0]
    M2x --- M0tq[M0]
    M2x --- M0tr[M0]
    M2y --- M0ts[M0]
    M2y --- M0tt[M0]
    M2z --- M0tu[M0]
    M2z --- M0tv[M0]
    M1u --- M0ta[M0]
    M1u --- M0tb[M0]
    M1v --- M0tc[M0]
    M1v --- M0td[M0]
    M1w --- M0te[M0]
    M1w --- M0tf[M0]
    M1x --- M0tg[M0]
    M1x --- M0th[M0]
    M1y --- M0ti[M0]
    M1y --- M0tj[M0]
    M1z --- M0tk[M0]
    M1z --- M0tl[M0]
    M1aa --- M0tm[M0]
    M1aa --- M0tn[M0]
    M1ab --- M0to[M0]
    M1ab --- M0tp[M0]
    M1ac --- M0tq[M0]
    M1ac --- M0tr[M0]
    M1ad --- M0ts[M0]
    M1ad --- M0tt[M0]
    M1ae --- M0tu[M0]
    M1ae --- M0tv[M0]
    M1af --- M0ta[M0]
    M1af --- M0tb[M0]
    M1ag --- M0tc[M0]
    M1ag --- M0td[M0]
    M1ah --- M0te[M0]
    M1ah --- M0tf[M0]
    M1ai --- M0tg[M0]
    M1ai --- M0th[H0]
    M1aj --- M0ti[M0]
    M1aj --- M0tj[M0]
    M1ak --- M0tk[M0]
    M1ak --- M0tl[M0]
    M1al --- M0tm[M0]
    M1al --- M0tn[M0]
    M1am --- M0to[M0]
    M1am --- M0tp[M0]
    M1an --- M0tq[M0]
    M1an --- M0tr[M0]
    M1ao --- M0ts[M0]
    M1ao --- M0tt[M0]
    M1ap --- M0tu[M0]
    M1ap --- M0tv[M0]
    M1aq --- M0ta[M0]
    M1aq --- M0tb[M0]
    M1ar --- M0tc[M0]
    M1ar --- M0td[M0]
    M1as --- M0te[M0]
    M1as --- M0tf[M0]
    M1at --- M0tg[M0]
    M1at --- M0th[M0]
    M1au --- M0ti[M0]
    M1au --- M0tj[M0]
    M1av --- M0tk[M0]
    M1av --- M0tl[M0]
    M1aw --- M0tm[M0]
    M1aw --- M0tn[M0]
    M1ax --- M0to[M0]
    M1ax --- M0tp[M0]
    M1ay --- M0tq[M0]
    M1ay --- M0tr[M0]
    M1az --- M0ts[M0]
    M1az --- M0tt[M0]
    M1ba --- M0tu[M0]
    M1ba --- M0tv[M0]
    M1bb --- M0ta[M0]
    M1bb --- M0tb[M0]
    M1bc --- M0tc[M0]
    M1bc --- M0td[M0]
    M1bd --- M0te[M0]
    M1bd --- M0tf[M0]
    M1be --- M0tg[M0]
    M1be --- M0th[M0]
    M1bf --- M0ti[M0]
    M1bf --- M0tj[M0]
    M1bg --- M0tk[M0]
    M1bg --- M0tl[M0]
    M1bh --- M0tm[M0]
    M1bh --- M0tn[M0]
    M1bi --- M0to[M0]
    M1bi --- M0tp[M0]
    M1bj --- M0tq[M0]
    M1bj --- M0tr[M0]
    M1bk --- M0ts[M0]
    M1bk --- M0tt[M0]
    M1bl --- M0tu[M0]
    M1bl --- M0tv[M0]
    M1bm --- M0ta[M0]
    M1bm --- M0tb[M0]
    M1bn --- M0tc[M0]
    M1bn --- M0td[M0]
    M1bo --- M0te[M0]
    M1bo --- M0tf[M0]
    M1bp --- M0tg[M0]
    M1bp --- M0th[M0]
    M1bq --- M0ti[M0]
    M1bq --- M0tj[M0]
    M1br --- M0tk[M0]
    M1br --- M0tl[M0]
    M1bs --- M0tm[M0]
    M1bs --- M0tn[M0]
    M1bt --- M0to[M0]
    M1bt --- M0tp[M0]
    M1bu --- M0tq[M0]
    M1bu --- M0tr[M0]
    M1bv --- M0ts[M0]
    M1bv --- M0tt[M0]
    M1bw --- M0tu[M0]
    M1bw --- M0tv[M0]
    M1bx --- M0ta[M0]
    M1bx --- M0tb[M0]
    M1by --- M0tc[M0]
    M1by --- M0td[M0]
    M1bz --- M0te[M0]
    M1bz --- M0tf[M0]
    M1ca --- M0tg[M0]
    M1ca --- M0th[H0]
    M1cb --- M0ti[M0]
    M1cb --- M0tj[M0]
    M1cc --- M0tk[M0]
    M1cc --- M0tl[M0]
    M1cd --- M0tm[M0]
    M1cd --- M0tn[M0]
    M1ce --- M0to[M0]
    M1ce --- M0tp[M0]
    M1cf --- M0tq[M0]
    M1cf --- M0tr[M0]
    M1cg --- M0ts[M0]
    M1cg --- M0tt[M0]
    M1ch --- M0tu[M0]
    M1ch --- M0tv[M0]
    M1ci --- M0ta[M0]
    M1ci --- M0tb[M0]
    M1cj --- M0tc[M0]
    M1cj --- M0td[M0]
    M1ck --- M0te[M0]
    M1ck --- M0tf[M0]
    M1cl --- M0tg[M0]
    M1cl --- M0th[H0]
    M1cm --- M0ti[M0]
    M1cm --- M0tj[M0]
    M1cn --- M0tk[M0]
    M1cn --- M0tl[M0]
    M1co --- M0tm[M0]
    M1co --- M0tn[M0]
    M1cp --- M0to[M0]
    M1cp --- M0tp[M0]
    M1cq --- M0tq[M0]
    M1cq --- M0tr[M0]
    M1cr --- M0ts[M0]
    M1cr --- M0tt[M0]
    M1cs --- M0tu[M0]
    M1cs --- M0tv[M0]
    M1ct --- M0ta[M0]
    M1ct --- M0tb[M0]
    M1cu --- M0tc[M0]
    M1cu --- M0td[M0]
    M1cv --- M0te[M0]
    M1cv --- M0tf[M0]
    M1cw --- M0tg[M0]
    M1cw --- M0th[H0]
    M1cx --- M0ti[M0]
    M1cx --- M0tj[M0]
    M1cy --- M0tk[M0]
    M1cy --- M0tl[M0]
    M1cz --- M0tm[M0]
    M1cz --- M0tn[M0]
    M1da --- M0to[M0]
    M1da --- M0tp[M0]
    M1db --- M0tq[M0]
    M1db --- M0tr[M0]
    M1dc --- M0ts[M0]
    M1dc --- M0tt[M0]
    M1dd --- M0tu[M0]
    M1dd --- M0tv[M0]
    M1de --- M0ta[M0]
    M1de --- M0tb[M0]
    M1df --- M0tc[M0]
    M1df --- M0td[M0]
    M1dg --- M0te[M0]
    M1dg --- M0tf[M0]
    M1dh --- M0tg[M0]
    M1dh --- M0th[H0]
    M1di --- M0ti[M0]
    M1di --- M0tj[M0]
    M1dj --- M0tk[M0]
    M1dj --- M0tl[M0]
    M1dk --- M0tm[M0]
    M1dk --- M0tn[M0]
    M1dl --- M0to[M0]
    M1dl --- M0tp[M0]
    M1dm --- M0tq[M0]
    M1dm --- M0tr[M0]
    M1dn --- M0ts[M0]
    M1dn --- M0tt[M0]
    M1do --- M0tu[M0]
    M1do --- M0tv[M0]
    M1dp --- M0ta[M0]
    M1dp --- M0tb[M0]
    M1dq --- M0tc[M0]
    M1dq --- M0td[M0]
    M1dr --- M0te[M0]
    M1dr --- M0tf[M0]
    M1ds --- M0tg[M0]
    M1ds --- M0th[H0]
    M1dt --- M0ti[M0]
    M1dt --- M0tj[M0]
    M1du --- M0tk[M0]
    M1du --- M0tl[M0]
    M1dv --- M0tm[M0]
    M1dv --- M0tn[M0]
    M1dw --- M0to[M0]
    M1dw --- M0tp[M0]
    M1dx --- M0tq[M0]
    M1dx --- M0tr[M0]
    M1dy --- M0ts[M0]
    M1dy --- M0tt[M0]
    M1dz --- M0tu[M0]
    M1dz --- M0tv[M0]
    M1ea --- M0ta[M0]
    M1ea --- M0tb[M0]
    M1eb --- M0tc[M0]
    M1eb --- M0td[M0]
    M1ec --- M0te[M0]
    M1ec --- M0tf[M0]
    M1ed --- M0tg[M0]
    M1ed --- M0th[H0]
    M1ee --- M0ti[M0]

```

BYE

N1

BYE

29/30 Playoff

BYE

O1

BYE

BYE

O3

BYE

O2

BYE

BYE

31/32 Playoff

BYE

P1

BYE